Department of Electronics & Communication Engineering
MNIT Jaipur

Patron
Udaykumar R Yaragatti
Director, MNIT Jaipur

Convener
Prof. Vijay Janyani
Head, ECE Department
MNIT Jaipur

Faculty Coordinators-
Dr. Menka
Assistant Professor
ECE Department
M: +91 9549650791
Email: menka.ece@mnit.ac.in

Dr. Chitrakant Sahu
Assistant Professor
ECE Department
M: +91-9549655371
E-mail– chitrakant.ece@mnit.ac.in

Student Coordinator-
Jaydeep Singh Parmar
M: +91-95092 91320
E-mail– 2016REC9510@mnit.ac.in
Basudha - 2019rec9012@mnit.ac.in
SHALINI CHAUDHARY- 2019rec9066@mnit.ac.in

Important Date
Last date of registration:
30-09-2020, 5:00 PM

Link to the registration form:
https://forms.gle/Z99JrBvmKrLcsJ8G7
Registration Charges:
- Rs. 100/- for MNIT Jaipur student
- Rs. 200/- for outside MNIT students
- Rs. 500/- for faculty

Online Payment Details:
A/c Name: The Registrar MNIT, Jaipur (TEQIP Phase-III)
Bank Name: State Bank of India (SBI)
Current A/c No.: 36875887782
IFSC Code: SBIN0015921
Branch Name: MNIT Campus, Jaipur
OR
PAYTM to 9509291320
(Jaydeep Singh Parmar)

Eligibility/Target Audience
- Faculty members/PhD Scholars/UG/PG Students of AICTE affiliated institutes.
- Applications will be accepted on **First cum first serve basis**.
- Certificates will be issued to the participants only after attending the complete course.

Five daysonline workshop from 1st-5th October, 2020 is organised by **Department of Electronics and Communication engineering, Malaviya National Institute of Technology Jaipur**. MNIT Jaipur is ranked 35thin NIRF 2020 among Top Engineering Institutions in India.This workshop will provide an opportunity to learn recent research and development in the area of VLSI devices/Integrated Circuits from eminent experts from IITs/NITs. The participants will have an exposure to the state-of-the-art in semiconductor device to circuit modelling, integrated circuits for Analog and Memory and applications. This workshop will provide discussion/presentation on future research directions in the field of VLSI chips and applications.

Topics to be Covered: (approx. 2.5 hours per module)
Module 1. Research trends in Emerging Semiconductor Device Technologies, Future Roadmap of Electronics Industry by Prof. SudebDasgupta, Head of ECE Department, IIT Roorkee
Module 2. Future trends in High Speed Memory Design, In Memory Boolean Computation for AI /ML Applications by Dr.Jawar Singh, Associate Professor, IIT Patna
Module 3. Role of Semiconductor Device Modeling in for future generation electronics - a case study. Tools for Device Simulation and Modelling by Dr.Menka Yadav, MNIT Jaipur
Module 4. Challenges in Analog IC Design in nanoscale CMOS/FinFET technologies, a case study on 180 nm CMOS PDK, EDA Tools for Circuit Design byDr.Subhakumar Reddy, Veda IIT Hyderabad
Module 5. ITRS Roadmap, Recent trends in Non classical CMOS devices such as Junction-less FET, Charge-Plasma FET and State of the Art Advanced Semiconductor Devices. TCAD Tools for Device Simulation by Dr.ChitrakantSahu, MNIT Jaipur

About the Department ECE
The department of Electronics and Communication Engineering started functioning in 1984. The department offers four-year courses leading to the Bachelor's Degree and also offer four Post Graduate Programs. It offers Ph.D. programs in various specializations like Artificial Intelligence, Device modelling, MEMS devices, optoelectronics, Antenna, RF-microwave, Wireless Networks, Image Processing, etc. The Department is equipped with state-of-the-art labs to support the UG, PG and Research Programs.

Venue/Mode:
The workshop will be organized using online **Platform**. The joining link and minute to minute time table will be shared one day before the workshop.

Speakers

Prof. SudebDasgupta
Head of ECE Department, IIT Roorkee

Dr. Jawar Singh
Associate Professor, IIT Patna

Dr. Subhakumar Reddy
Veda IIT Hyderabad

Dr. Chitrakant Sahu
MNIT Jaipur

Dr. Menka Yadav
MNIT Jaipur

NOTE- Contact to coordinators for any query regarding the workshop.