



Malaviya National Institute of Technology, Jaipur
Department of Electronics & Communication Engineering

Applications are invited for the Post of Junior Research Fellow
(on contract basis)

Applications are invited for the post of Junior Research Fellow in Department of Electronics and Communication Engineering, MNIT, Jaipur under project entitled “Charge pump PLL frequency synthesizer design” funded by Regional Academic Centre for Space (RAC-S) Department of Space, Govt. of India.

The details are as under:

S. No.	Designation	No. of Posts	Essential Qualification	Desirable	Stipend per month	Age Limit
1	Junior Research Fellow	One	1. B.E/B.Tech in Electronics and Communication Engg or equivalent, with at least 60% marks and 2. M.E. / M. Tech. in VLSI Design or Microelectronics or Electronics and Communication or Information & Communication Technology or equivalent, With a minimum CGPA of 7.0	<ul style="list-style-type: none">• Experience in Design of Analog Integrated Circuits• Good command on various VLSI CAD tools like Cadence/ Mentor Graphics / Synopsys	Rs.31000/- + HRA(@16%)	35 Years

Essential Information:

1. **Cut-off Date:** Age and Education qualification will be counted as on 07/Nov/2021. Age Limit is relaxable by 5 years for SC/ST and 3 years for OBC candidates.
2. **Place of work:** Place of work is MNIT, Jaipur / SCL Chandigarh.
3. **Tenure:** Tenure of JRF is initially 1 year, which is extendable to maximum by one more year (i.e. total of two years) as per Govt rules. Award of Fellowship/Associate does not confer any right to absorption or employment in MNIT Jaipur.
4. **How to apply:** Apply online on the link given below latest by 07/Nov/2021 at the weblink. No application will be entertained after the closing date.
5. <https://forms.gle/d7eXojVqg5H1gvku7>
6. **Date of Interview:** Date and time for Interview will be communicated individually to the suitable
7. candidates by email.
8. **Interview:** Interview will be conducted Online or Off-line as per provisions available at that time.

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