

**Provisional List of the Eligible/Ineligible candidate for written test/Interview in the
Department of ELECTRONICS AND COMMUNICATION ENGINEERING for Ph.D. Admission EVEN Semester 2025-26 (updated 04-12-2025)**

Sr No	Form No	Applicant Name	Department	Admission Category	Admission Sub Category	Eligible/ Ineligible	Reason for Ineligible	Entrance written Test waived off (Yes or No) if yes give reason	Remarks
1	PHD/2025/7088	OM SINGH	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
2	PHD/2025/7236	PANKAJ SHARMA	Electronics & Comm. Engineering-ECE	Off Campus	Off Campus (Off Campus (beyond 70 km from Jaipur)	Provisionally Eligible		No	
3	PHD/2025/7317	BHEEMRAJ MEENA	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		Yes	GATE 2019, Mtech 2021
4	PHD/2025/7453	SANDESH SINGH SHEKHAWAT	Electronics & Comm. Engineering-ECE	Part Time	Institute Project Staff (Working in MNIT Jaipur)	Provisionally Eligible		Yes	GATE 2020, Mtech 2023
5	PHD/2025/7519	AASHISH KUMAR SHARMA	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
6	PHD/2025/7687	DIVANSHU JAIN	Electronics & Comm. Engineering-ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Provisionally Eligible		No*	GATE 2010, Mtech 2011
7	PHD/2025/7896	KAVITA CHAUTHANI	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		Yes	GATE 2010, Mtech 2013
8	PHD/2025/7901	RIYA SONI	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	Applied for Direct PhD. Btech from Bikaner Technical University (NIRF is 109)	NA	
9	PHD/2025/7085	JATAV VAISHALI MANSINGH	Electronics & Comm. Engineering-Embedded Systems	Full Time	Full Time with own scholarship (with UGC/CSIR/NET-JRF/National Level Exam/Visvesvaraya Scheme)	Ineligible	No GATE/ NET score	NA	
10	PHD/2025/7716	AARADHYA	Electronics & Comm. Engineering-Embedded Systems	Off Campus	Off Campus (Off Campus (beyond 70 km from Jaipur)	Ineligible	No experience	NA	
11	PHD/2025/7106	NARESH MEGHWAL	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		Yes	GATE 2019, Mtech 2022

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12	PHD/2025/7238	JITENDRA SINGH KUNTAL	Electronics & Comm. Engineering-VLSI Design	Part Time	Part Time (candidate working within 70 km of Jaipur)	Provisionally Eligible		No	
13	PHD/2025/7255	KINNAL MEHTA	Electronics & Comm. Engineering-VLSI Design	Part Time	Institute Staff (Working in MNIT Jaipur)	Provisionally Eligible		No	
14	PHD/2025/7468	SHAHARE SAGAR PRASHANT	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		Yes	GATE 2023, Mtech 2025
15	PHD/2025/7787	SANJAY KUMAR BAGARIA	Electronics & Comm. Engineering-VLSI Design	Part Time	Institute Staff (Working in MNIT Jaipur)	Provisionally Eligible		No	
16	PHD/2025/7830	MANGILAL MEGHWAL	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
17	PHD/2025/7852	VIPRA BOHARA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
18	PHD/2025/7858	RASHMI SHRIVASTAVA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
19	PHD/2025/7860	ALKA RANI	Electronics & Comm. Engineering-VLSI Design	Part Time	Part Time (candidate working within 70 km of Jaipur)	Provisionally Eligible		No	
20	PHD/2025/7878	ANURAG DASH	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
21	PHD/2025/7892	DHIRAJ KUMAR	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with own scholarship (with UGC/CSIR/NET-JRF/National Level Exam/Visvesvaraya Scheme)	Provisionally Eligible		Yes	GATE 2023, Mtech 2025
22	PHD/2025/7034	KAVISHA YADAV	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	
23	PHD/2025/7326	YASH KATYAYAN MISHRA	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	

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24	PHD/2025/7327	SOURAV KUMAR NAWANI	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	